PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Cheng-Tung Huang et al.

5 Patent No.: US 6,955,929 B2 Issue Date: 10/18/2005

Serial No.: 10/709,236 Filing Date: 04/23/2004

Examiner: NGUYEN, TUAN H Art Unit: 2813

Docket No.: NAUP0464USA

10 Title: METHOD OF MEASURING A GATE CHANNEL LENGTH OF A

METAL-OXIDE SEMICONDUCTOR TRANSISTOR

To: Commissioner for Patents

P.O. BOX 1450

15 Alexandria, VA 22313-1450

Subject: Status Inquiry

Dear Sir,

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The applicant submitted a request to correct inventorship under 37 CFR 1.324(b) on 01/16/2006 and the Petition Decision mailed on 04/10/2007 was received. However, the Certificate of Correction has not been received so far.

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The third inventor <u>Chien-Chieh Huang</u> was recorded as one of the inventors on 01/17/2006 but the Inventors Information showed on PAIR remains unchanged.

Total Assignments: 2 Application #: 10709236 Filing Dt: 04/23/2004 Patent #: 6955929 Issue Dt: 10/18/2005 PCT #: NONE Publication #: US20040214356 Pub Dt: 10/28/2004 Inventors: Cheng-Tung Huang, Sheng-Hao Lin, Nien-Chung Li, Yi-Cheng Sheng Title: METHOD OF MEASURING A GATE CHANNEL LENGTH OF A METAL-OXIDE SEMICONDUCTOR TRANSISTOR Assianment: 1 Reel/Frame: 014525 / 0410 Received: 04/23/2004 Recorded: 04/23/2004 Mailed: 04/23/2004 Pages: 3 Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS), Assignors: HUANG, CHENG-TUNG Exec Dt: 04/21/2004 LIN, SHENG-HAO Exec Dt: 04/21/2004 LI, NIEN-CHUNG Exec Dt: 04/21/2004 SHENG, YI-CHENG Exec Dt: 04/21/2004 Assignee: UNITED MICROELECTRONICS CORP. NO.3, LI-HSIN ROAD 2, SCIENCE-BASED INDUSTRIAL PARK HSIN-CHU CITY, TAIWAN Correspondent: NAIPO (NORTH AMERICA INTERNATIONAL PATEN P.O. BOX 506 MERRIFIELD, VA 22116 Assignment: 2 Reel/Frame: 017468 / 0752 Received: 01/23/2006 Recorded: 01/17/2006 Mailed: 04/14/2006 Pages: 4 Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS). Assignors: HUANG, CHENG-TUNG Exec Dt: 12/13/2005 LIN, SHENG-HAO Exec Dt: 12/13/2005 HUANG, CHIEN-CHIEH Exec Dt: 12/13/2005 LI, NIEN-CHUNG Exec Dt: 12/13/2005 SHENG, YI-CHENG Exec Dt: 12/13/2005 Assignee: UNITED MICROELECTRONICS CORP. NO. 3, LI-HSIN ROAD 2, SCIENCE-BASED PARK HSIN-CHU CITY, TAIWAN R.O.C. Correspondent: NORTH AMERICA INTELLECTUAL PROPERTY CORP P.O. BOX 506 MERRIFIELD, VA 22116

Please add the third inventor <u>Chien-Chieh Huang</u> as one of the inventors of the above-identified utility patent and issue the Certificate of Correction as soon as possible. Your quick response will be greatly appreciated.

Respectfully submitted,

Weinton Har Date: 09/04/2007

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Note: Please leave a message in my voice mail if you need to talk to me. (The time in D.C. is 12 hours behind the Taiwan time, i.e. 9 AM in D.C. = 9 PM in Taiwan.)